

Xenomai-based multiple-process system, for real-time data acquisition and graphical display control

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Abstract

To elicit complex and rich graphical displays, and record neuronal phenomena of interest while all simultaneously being capable to interact in a closed-loop with external devices is a challenging task to all neurophysiologists. To facilitate this process, we have developed an Open-Source software system using a single computer running a well established Linux architecture (Ubuntu) associated to a kernel duo providing hard real-time support (Xenomai). We show that a single computer using our API is capable, for any tasks that require OpenGL displaying, to achieve millisecond accuracy programmed events. In this report, we describe the design of our system, benchmark and its performance in a real-world setting, and describe some key features.

Keywords: Realtime, Visual display, Neurophysiology

1. INTRODUCTION

Eliciting graphical displays, while simultaneously recording neuronal phenomena of interest and interact in a closed-loop with some external devices is a challenging task to realize in a standardized commonly used computer. One of the first well-known systems to accomplish this task was the complex UNIX-based real-time PET application developed for oculomotor experiments by Hays et al. (1982). As new applications have continued to emerge, however, many have forsaken true (or hard) real-time control and response in favor of simplicity, extended functionality, alternative operating systems, and/or user-friendly design. Those systems that have maintained real-time support (e.g., TEMPO, ePrime, Matlab Psychtoolbox) are often costly, proprietary, and require additional code in order to define specific experiments. Finally, other systems utilizing the proven LabVIEW development architecture (e.g., Kodosky and Dye, 1989; Kullmann et al., 2004; Poindessault et al., 1995; Pruehsner et al., 2003; Sakatani and Isa, 2004; Gandhi and Bonadonna, 2004) was developed and optimized. However while these systems offer open access configuration, displaying are largely limited due to an LED board design necessary to a system refresh rate of 1 kHz and lacking monitor driver's development. By creating an open source system that has minimal programming overhead and allow OpenGL graphical development, we hoped to allow users to focus on the essential features of experimental design and the basic elements of behavioral control and monitoring rather than on the often arcane details of the video presentation and data acquisition hardware. Our major goals were:

- To allow behavioral control with high temporal precision in free open source resources.
- To allow straightforward scripting of behavioral tasks using OpenGL and C++ syntax and functions.

- To interface transparently with data acquisition hardware for input / output functions, such as analog continuous signal, joystick and button-press acquisition, reward delivery, digital event marker output, as well as analog and TTL output to drive stimulators and injectors.
- To allow the full reconstruction of task events from the behavioral data file by including complete descriptions of behavioral performance, the event markers and their text labels, the task structure, and the actual stimulus images used; as a demonstration of this goal, to allow the replaying of any given trial from the behavioral data file alone.
- To provide the experimenter with an information rich display of behavioral performance and to reflect task events in real-time to aid the assessment of on-going behavior.

Rexeno is a software we developed that seeks to bind all these characteristics into a single piece of program, using a single computer, taking advantage of the best Open Source resources available. The main characteristic of this program is that it will be able to interact in real-time with every resources connected to it (Screens, A/D cards, Hard Drive, etc...).

Our tested system was composed of a Dell Computer with an Intel Duo processor (model E5405) running at 2.00 GHz and containing 2048MB of RAM (Dell Inc., Round Rock, TX). The graphics hardware in this machine consisted of an nVidia ENG250 silent with 1024MB of video RAM. Output from this dual-headed graphics card was split to two subject displays running in full-screen mode at pixel resolutions of 1024 768. The displays were standard cathode-ray tubes measuring 19 inches in the diagonal, also from Dell. The refresh rate for the tests reported here was 60 Hz and the experimenters display window was set to update every 16.6 ms during behavioral monitoring to allow near-real-time ($< 1\text{ms}$) observation of the subjects performance.

To assess the performance of our software, we first collected data from a photodiode coupled with our neurophysiological recording system (Plexon Inc, TX, USA). Thus, we analyzed data simple from the on-going training of two rhesus monkeys (macaca mulatta, male, respectively 10.5 and 16 Kg) in a saccade inhibitory task.

For the animal testing and in order to allow eye-tracking, head fixation was achieved using a titanium head-post system (Crist instrument, Hagerstown, MD, USA). Visual fixation was required for a total of 3s (about 1 s of initial fixation followed by a 1-1.500 ms target presentation). An inter-trial-interval of 1 to 2 s was used. Data from three consecutive months of training (one session each day) were collected and confirmed to yield nearly identical results, so one of these sessions was chosen arbitrarily for presentation below. This session consisted of 1500 trials over 2 hours. At all times, the animal was handled in accord with EU guidelines and those of the ICM Animal Care and Use Committee. Analog X and Y position signals conveying behavioral output consisted of an optical eye-tracking system (Eyelink 2k, SR Research Ltd., Mississauga, Ontario, Canada) running at 1000 Hz. For a more straightforward demonstration, a schematic diagram, the code (timing script) and the conditions file for a simpler task (a standard pro-saccade task) is shown in Figure ?? . The object of this paper is to explain how we designed and tested our Real Time System, then we will detail how to obtain and use it. Usability is a critical issue for us because we needed powerful tools designed by others in order to build Rexeno, and we hope it will also be adapted by others in order to help them in their respective tasks.

2. Material and Methods

2.1. Existing alternatives

One millisecond is a relatively coarse (small ?) unit of measure by electronic standards. However such temporal precision on a non-hard-real-time system, running on most popular operating systems (Windows, Mac, Unix), has no guarantees, because the predictability of software events is limited by the design of the operating system (OS). Specifically, even those processes designated as having a real-time priority can be pre-empted by both kernel-level events and by interrupt requests, as well as by other processes with equally high-priority (Ramamritham et al., 1998). While using systems with multiple processors may provide some benefit, they do not alter the fundamental problem. System developers have no way to control the hardware outside of the capabilities provided by the running OS.

64 2.1.1. *Rex*

65 Rex is a major solution in the world of Sensorimotor Research. Based on a QNX RTOS, it is capable of displaying
66 events, interacting with DIO, AIO, with a sub-millisecond reliability. It was our main inspiration for this project and
67 most of our objectif was to program a free alternative containing as many elements from Rex as possible, in an open,
68 improvable, configurable framework.

69 2.1.2. *Matlab & PsychoToolBox*

70 Matlab is a possible platform for such projects, nevertheless, there are notable limitations. Windows or Mac
71 cannot support hard real-time operation. Therefore, while sub-millisecond jitter is acceptable in many, if not most,
72 psychophysical settings, there are nonetheless many potential applications for which the software described here
73 would not be suitable. In particular, experiments that must provide feedback within a very small temporal window
74 (for example, to influence an on-going synaptic event) would find 2-5 ms jitter simply too variable. Furthermore,
75 likewise, delivering feedback that requires a great deal of processing could potentially incur unacceptably long delays.
76 There is a potential risk of blind period at the beginning of each behavioral tracking episode. Other limitations include
77 the current inability to display movies or translating visual stimuli while simultaneously tracking behavioral signals.
78 In addition, behavioral signals are not currently stored during the inter-trial interval. The ability to store analog
79 signals continuously would benefit not only behavioral signals, but neurophysiological ones as well. In other words,
80 although many acquisition cards are clearly capable in terms of number of channels, sampling rates and PC storage
81 of recording neural data alongside behavioral signals, no support has been built-in for this purpose. Such a capability
82 would likely be useful for many potential applications.

83 2.2. *Our Solution : Rexeno*

84 2.2.1. *General Presentation*

85 We designed Rexeno's structure to be flexible and compatible for many different types of experiments. One can
86 plug about any analogical signal as an input (in Figure 1, we listed EEG, Eye Movements and Respiratory Movements,
87 but these are just examples.) We also wanted to make the system compatible with any other hardware that requires
88 triggering through digital signals (for example : Electrical stimulator devises, Transmagnetic stimulator, or Plexon
89 hardware for single unit, local field potential or electroencephalographical signals). We will show in subsection 4.2
90 how to configure these Strobes with Rexeno.

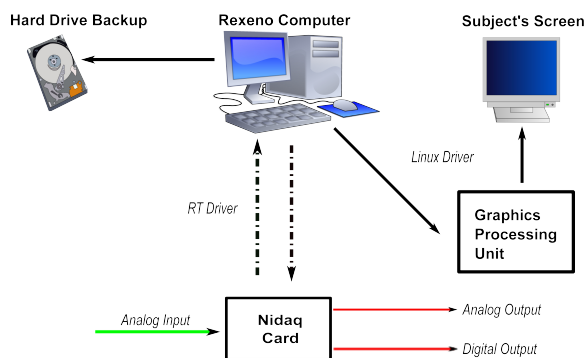


Figure 1. Rexeno Setup

91 2.2.2. *Hardware*

92 Prioritizing portability, we interfaced high quality hardware that was already commonplace in the world of visual
93 cognition study. Here is a list of the hardware used for our system :

- 94 • **Display :** CRT Screen
- 95 • **Data Acquisition :** National Instruments DAQ NI 6220

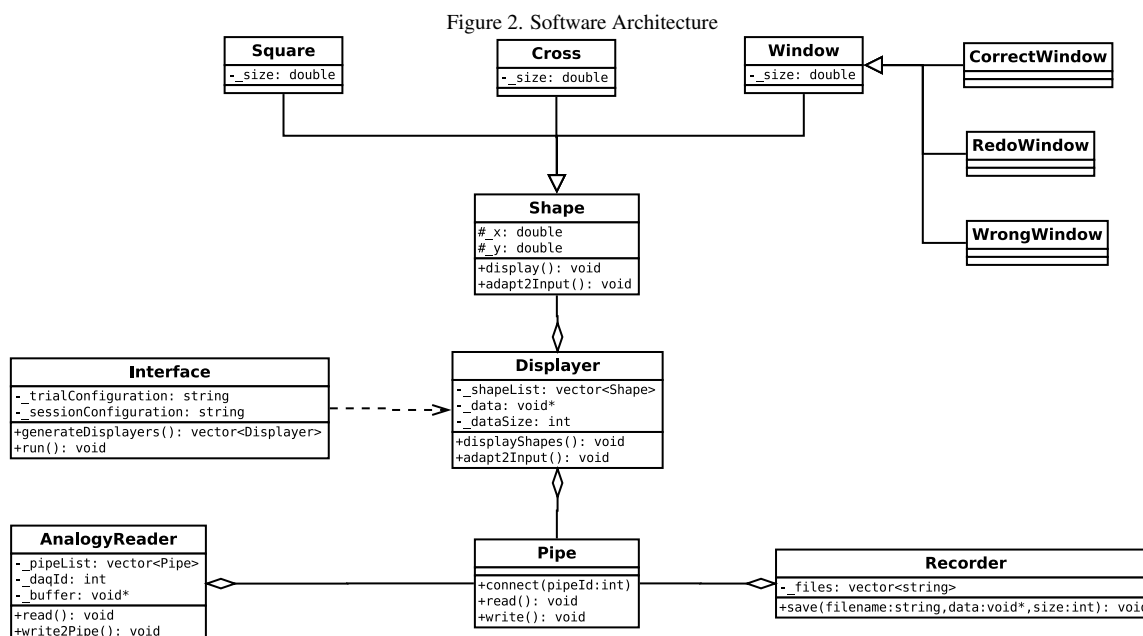
- **Graphic Card** : NVidia

- **CPU** : Intel Xeon E5405

2.2.3. Software & Solutions

Rexeno uses several freely available libraries, compatible with mainstream and high quality hardware, in order to obtain the best performances possible. Here are the main software resources that we used in order to create our program :

- **Xenomai 2.6.0** : One of the most interesting options in Real-Time Operating Systems. Enables us to control software behaviour down to the μ sec. Also provides real-time drivers for Analog/Digital interactions with AD cards.
- **OpenGL 2.0** : The main open-source graphical library. This 2.0 version allows us to display about anything and also provides powerful solutions to accelerate complicated rendering calculation with the graphic card using a dynamic pipeline (shader programming with GLSL)



2.2.4. Verification methodology

We use this software on several different machines dedicated to neurophysiology in humans or non-human primates. This software has been very adept at the creation of basic sensori-motor tasks, and is especially useful for the creation of cognitive tasks with greater numbers of stimuli and contingencies. These tasks are often coded within an hour, and modifications are simple to test. Because different behavioral tasks can potentially place heavy demands on different aspects of the operating system and hardware (e.g., varying graphics, disk and memory use), end-users should not take observed timing accuracy in one task as direct evidence of satisfactory accuracy in another; thorough testing must be performed to assess the performance of new behavioral paradigms and new hardware configurations. The occurrence of temporal slips (unexpectedly increased latencies) often can be detected using time-stamps placed after critical behavioral events. These mark an event with reference to the deterministic system clock. A delay in the appearance of an expected time-stamp can then be used to reject trials in which timing constraints were not met. Of course, a delayed time-stamp could also represent a false-alarm when the delay occurred in the processing of that time-stamp itself and not in the preceding event. Fortunately, as we found above, such temporal slips can be made

121 very infrequent, and are rarely longer than a millisecond. Once appropriate care has been taken to ensure accuracy in
122 the three domains that are most likely introduce temporal jitter and error (video output, data sampling, and software),
123 the reliance on a high-level language for behavioral control offers numerous bene-fits aside from simply the ease of
124 task coding and portability across a wider range of hardware platforms. In particular, the simplicity with which new
125 features can be coded encourages the development of new functions that improve usability and record keeping. While
126 in principle such benefits could be realized in a low-level language, in practice, the difficulty and time-consuming
127 nature of programming in such a language hinders their development by those who would like to spend their time
128 designing and carrying out experiments rather than tweaking software.

129 3. Constraints

130 3.1. Definitions

131 As can be seen (in Figure 1), the interaction with the environment can be summerized with the following outputs
132 :

- 133 • **Display** : What appears on the subject’s screen.
- 134 • **Backup** : What was recorded by the machine
- 135 • **Digital Triggers** : Sent by the NIDAQ card.

136 Rexeno’s goal is to bring Hard Real-Time capability to these outputs.

137
138 Definition : A program is said to respect hard real time constraints when it’s time of execution is deterministic.

139 The current system uses Xenomai’s Analogy Drivers for the interaction with NIDAQ card, so we know than these
140 programs are real time compatible[?]. The interaction with the screens use NVidia proprietary drivers that do not
141 offer real time guarentee (which is normal, this was not what they were designed for). Because of these drivers, it is
142 not possible today to guarentee hard real time constraints while displaying something on a screen, what our tests will
143 have to do is define under what conditions, jitters are still acceptable.

144 3.2. Testing the system

145 In order to verify the accuracy of our system, we designed the Double Flash Protocole, which consists of 2000
146 trials. Each trial consists of two successive flashes (duration = 50 frames \approx 833 ms). Time between front edge of
147 the flash is 100 frames (\approx 1666.667ms). A photodiode is placed on the screen so that the flash creates a tension at
148 the terminals of the photodiode. This tension was recorded at the same time by the Rexeno System, and by a Plexon
149 Acquisition Unit. We can see a typical trial recorded on the Plexon unit in Figure 3a.

150
151 It is designed to evaluate jitter between input and :

- 152 • Hard Drive Backup
- 153 • Displaying
- 154 • Digital output

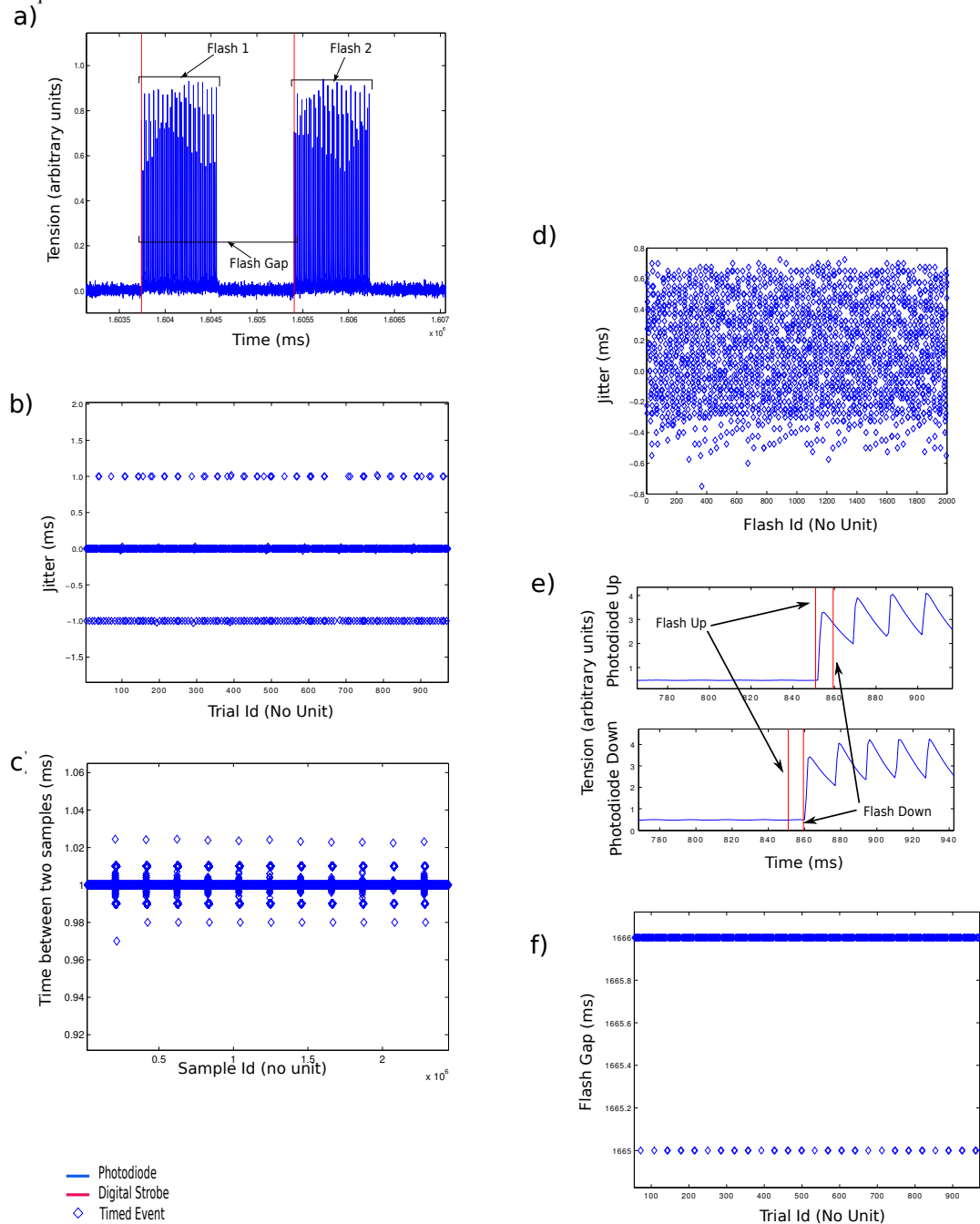
155 3.3. Specific Hardware Interactions

156 3.3.1. Hard Drive Backup

157 Description

158 . In a given trial, we wish to evaluate how precise the hard drive recording is. This task requires interaction with the
159 Nidaq card and with the Hard Drive hardware. As said before, the Nidaq card has Real-Time compatible hardware
160 (the Analogy drivers). This is not the case of the Hard Drive which cannot write in real time. This could be a problem
161 because RT program is only as strong as it’s weakest link. A “seek” operation requires about 5ms (depending on the
162 hardware configuration[?]), which is obviously not enough for 1000 Hz recording (writing on several files might

Figure 3. a,b,d,f - Two flashes at the same position separated by a time gap e - Two flashes on the same display frame but different positions c - Regular acquisition



163 create several “seek” operations on the hard drive at a given time, leading to data loss). Our solution was to use the
164 IOstream C++ Library, which creates a buffer capable of saving on RAM memory large quantity of data and writing
165 several Nidaq acquisitions to the disk using a single “write” operation. Another hardware specification indicates :
166 “Host to/from drive (sustained) = 200Mb/sec“.

167 On the other hand, we aim for a 1000 Hz acquisition frequency (on 8 analogy channels) which creates the following
168 quantity of data :

169 With :

- 170 • $N_{Channels} = 8$
- 171 • $Size_{Single_Acquisition} = 8 \text{ Bytes}$
- 172 • $Frequency = 1000Hz$
- 173 • Θ represents the data’s encoding function¹

174 Thanks to (3), we know we should be able to write all data in time without any loss (provided we use the streaming
175 buffer technique). In the next two paragraphs, we will check that this was correctly implemented.

177 *Extracting data*

178 . In order to check that the recorded manifestations have coherent timestamps, we decided to create an algorithm that
179 will detect the Flash’s relative timestamps.

180 The result of this algorithm is shown in Figure 3b.

181 Our acquisition frequency being 1000 Hz (on the plexon and on the rexeno system,) the ± 1 ms jitter is expected
182 and compatible with Visual Cognition Studies (a visual saccade has a typical duration of 10ms).

183 *3.3.2. On-Screen Displaying*

184 Displaying a stimuli is a task that requires interaction with a screen. This is done using the OpenGL2.0 library
185 which will interact with the Nvidia graphic card’s drivers. The problem is the same as it was with the Hard Drive : the
186 driver is not real-time compatible. In order to obtain deterministic displaying of stimuli, we took advantage of the
187 CRT screen hardware which functions with a 60Hz displaying frequency. Our technique was simple and exploited the
188 OpenGL’s double buffer capacity : if the protocol needs to draw a stimuli at the n^{th} frame, we wait until the $(n - 1)^{th}$
189 frame and draw the corresponding stimuli on the back buffer which will automatically be displayed on the CRT screen
190 at the next frame.

191 The flash gap was supposed to be 100 frames at 60Hz, On Figure 3f is the evaluation of this gap using the Plexon
192 recording and the FrontEdge algorithm. The 1000Hz acquisition frequency creating again a ± 1 ms jitter.

193 *3.3.3. Digital Output*

194 Triggering digital output uses only the Analogy RT drivers. So we can control time very precisely. In order to
195 trigger events with displayed stimuli, we wait for a vBlank synchronisation to occur, wait a certain amount of time
196 depending of the CRT’s frequency and send the digital pulse. To evaluate the jitter, we recorded the strobe events
197 on the plexon and compared these timestamps to the ones returned by our FrontEdges function. The result of this
198 difference can be seen on Figure 3b.

199 *3.3.4. Vertical Synchronisation & Tearing*

200 In order to fully control what is displayed on the subject’s screen, we had to use the VBlank synchronisation of the
201 NVidia drivers. If enabled, the graphic card waits for the VBlank event (cathod ray beam is turned off for repositioning
202 at the top left of the screen) before starting to draw anything. This avoids the tearing effect. We used an experiment
203 where two flashes (one on the “up” half of the screen, the other on the “bottom” side) were supposed to appear on
204 screen at the same frame. Theoretically, the “up” flash should appear before the “down” flash because of the VBlank
205 synchronisation. With a photodiode placed at various positions, we decided to verify that assertion. Results are on
206 Figure 3e.

¹Depends on the entropy but it’s asymptot should be a constant

207 3.4. Conclusion

208 Even if it is impossible today to control in Hard Real Time every feature of the Rexeno station, we can emulate
209 real time by exploiting various techniques on a regular x86 computer.

210 4. How to use Rexeno

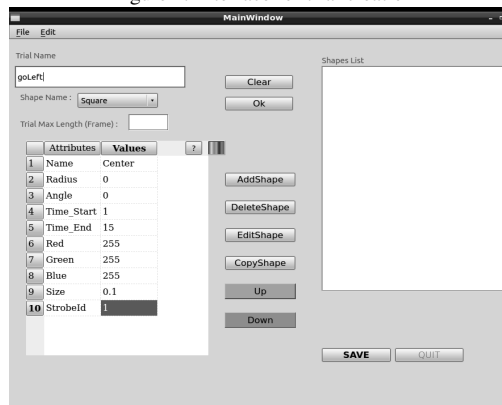
211 4.1. Using Rexeno

212 As we have seen, creating a fully reliable real time environment can be a challenging task. That is why we have
213 decided to fully package all these algorithms into one piece of software and distribute it freely to anyone who would
214 be interested. Making it, we hope, a good choice for people interested in setting up an experiment where precise
215 timing is necessary.

216 4.2. Defining a task

217 The user can define his protocole though a GUI that creates configurations files. These will be in charge of
218 describing the different trials.

Figure 4. Interface for trial creation



219 Here is an example of a configuration file created by the interface :

```
220  
221 idtrial1 300 GO_TARGET1  
222 id1 Square Target 0.97 0 End_Fixation End_Target 0 255 0 0.03 3  
223 id2 Cross Eye 0 0 0 10000 255 0 0 0.1  
224 id3 FixationWindow Fixation 0 0 0 End_Fixation 255 255 0 0.32 0.32 Fixation_Duration  
225 id4 NeutralWindow Neutral 0 0 0 End_Fixation 0 200 0 0.32 0.32  
226 id5 RedoWindow Redo 0 0 100 End_Fixation 200 0 0 2 2 0  
227 id6 Square Fixation 0 0 0 End_Fixation 0 255 0 0.03 1  
228 id7 CorrectWindow CorrectWindow 0.97 0 End_Fixation End_Target 255 255 0 0.5 0.5 Target_Time  
229 id8 Time 300  
230 id9 Variable GO_TARGET1 1  
231 endtrial
```

232
233 With such a file, we can launch the task with a subject. The main events are presented in Figure ??

